

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (Currently Amended) A terminal comprising:

a processor,

a memory,

a browser function running via said processor and said memory for browsing a network,

and

a man-machine-interface for controlling said browser function, [[and]]

a detector which detects whether a browser hang-up event has occurred, and

a generator for automatically generating an overrule signal for overruling said browser function in the case where [[a]] said detector detects that said browser hang-up event has occurred, said browser hang-up event blocking operation of said browser function.

2. (Previously Presented) The terminal according to claim 1, further comprising a transmitter for transmitting an information signal to a network-unit in response to said overrule signal, wherein said network-unit comprises a transmission unit for sending a response signal to said terminal for correcting said browser hang-up event in response to said information signal.

3. (Previously Presented) The terminal according to claim 1, wherein said overruling comprises a generation of a previous address signal.

4. (Previously Presented) The terminal according to claim 1, wherein said overruling comprises a reset of said browser function.

5. (Previously Presented) The terminal according to claim 1, wherein said overruling comprises terminating said browser function.

6. (Previously Presented) A network unit for use in combination with a terminal, said terminal comprising:

a processor,

a memory, a browser function running via said processor and said memory for browsing a network,

a man-machine-interface for controlling said browser function,

a generator for generating an overrule signal for overruling said browser function in the case where a browser hang-up event has occurred, said browser hang-up event blocking operation of said browser function, and

a transmitter for transmitting an information signal to said network-unit in response to said overrule signal, and

said network-unit comprising a transmitter unit for sending a response signal to said terminal for correcting said browser hang-up event in response to said information signal.

7. (Previously Presented) The network unit according to claim 6, wherein said overruling or said correcting comprises a generation of a previous address signal.

8. (Previously Presented) The network unit according to claim 7, wherein said overruling or said correcting comprises a reset of said browser function.

9. (Currently Amended) A method for use in combination with a terminal comprising a processor, a memory, a browser function running via said processor and said memory for browsing a network, and a man-machine-interface for controlling said browser function, said method comprising:

detecting whether a browser hang-up event has occurred;

automatically generating an overrule signal for overruling said browser function in the case where [[a]] it is detected that said browser hang-up event has occurred, said browser hang-up event blocking operation of said browser function.

10. (Currently Amended) A computer program product for performing a browser function in a terminal comprising a processor, a memory for running said browser function for browsing a network, and a man-machine-interface for controlling said browser function, said

computer program product comprising a detector function which detects that a browser hang-up event has occurred, and a generator function for automatically generating an overrule function for overruling said browser function in the case where [[a]] said detector function detects that said browser hang-up event has occurred, said browser hang-up event blocking operation of said browser function.

11. (New) The terminal according to claim 1, wherein said overrule signal is one of a memory address signal supplied to said memory, or a processor control signal supplied to said processor which generates said memory address signal supplied to said memory in response to said processor control signal, and said memory generates a predetermined network address signal to be transmitted to said network in response to said memory address signal.

12. (New) The method according to claim 9, wherein said overrule signal is one of a memory address signal supplied to said memory, or a processor control signal supplied to said processor which generates said memory address signal supplied to said memory in response to said processor control signal, and said memory generates a predetermined network address signal to be transmitted to said network in response to said memory address signal.

13. (New) The computer program product according to claim 10, wherein said overrule signal is one of a memory address signal supplied to said memory, or a processor control signal supplied to said processor which generates said memory address signal supplied to said memory

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in response to said processor control signal, and said memory generates a predetermined network address signal to be transmitted to said network in response to said memory address signal.